

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: JAMES MALIGEORGOS ET AL.

Filed: DECEMBER 8, 2003

For: PARTITIONING OF RADIO-FREQUENCY APPARATUS

Serial No.: 10/730,404

Group Art Unit: 2618

Examiner: GESESSE, T.

Atty Dkt: SILA:127C1

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA, 22313-1450 on the date below:

12/12/06
Date
Marty Jones
Name

MAIL STOP Issue Fee
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

COMMENTS ON THE STATEMENT OF REASONS FOR ALLOWANCE

As noted in MPEP 1302.14:

The statement [of reasons for allowance] is not intended to necessarily state all the reasons for allowance or all the details why claims are allowed and should not be written to specifically or impliedly state that all the reasons for allowance are set forth.

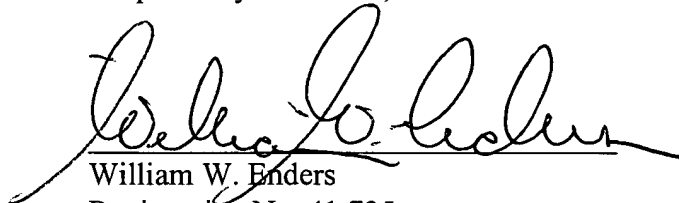
As such, the applicants understand that the allowed claims are allowable based on the claim language for each claim and that the reasons for allowance are not intended to fully express all the reasons for allowance. The applicant respectfully submits that the statement for reasons of allowance are not necessarily comprehensive for any individual claim and are not necessarily the sole reasons applicable to all of the claims. As a non-inclusive example, applicants note that allowed independent claim 47 recites "wherein said integrated DAC circuitry is configured to

receive one or more digital frequency control signals from said baseband processor circuitry and to generate and provide at least a portion of said one or more analog frequency control signals to said external frequency modification circuit based on said one or more digital frequency control signals” and allowed independent claim 57 recites “wherein said integrated DAC circuitry is configured to receive one or more digital frequency control signals from said baseband processor circuitry and to generate and provide at least a portion of said one or more analog frequency control signals to said DCXO circuitry based on said one or more digital frequency control signals”, rather than reciting “the integrated DAC circuitry is configured to receive one or more digital signals from the base band processor circuitry and generate at least a portion of the one or more analog frequency control signals and digital frequency control signals” as stated in the statement for reasons of allowance.

If the Office disagrees with these comments, the Office is requested to respond.

Should any fees under 37 CFR 1.16-1.21 be required for any reason relating to the enclosed materials, the Commissioner is authorized to deduct such fees from Deposit Account No. 10-1205. The examiner is invited to contact the undersigned at the phone number indicated below with any questions or comments, or to otherwise facilitate expeditious and compact prosecution of the application.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'William W. Enders', written over a horizontal line.

William W. Enders
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